

ABSTRACT OF THE DISCLOSURE

Disclosed is a synchronization system in digital communication, which comprises a converter for receiving signals from a transmitter, and oversampling a single symbol interval into a plurality of subsamples; a signal processor for classifying each symbol oversampled by the converter into a subsample group according to a sample phase, and performing signal processing to adjust processing speeds; an integrator for removing noise from the signals output by the signal processor and performing integration during a predetermined time; and a timing selector for selecting an optimal symbol synchronization point from among values output by the integrator, generating a symbol timing signal and outputting it.